

Name of Faculty	Prof. Saurabh P Shah
Designation	Assistant Professor
Email id	saurabhshah.ec@bitseducampus.ac.in
Contact number	9427540999
Total Experience	
Teaching	12 years 03 months
Industry	02 years 11 months
Professional Summary	Working as Assistant Professor in E&C Department of BIT from July – 2009. Served as second shift Head of the Department (2013-2015). Worked at various Positions like PG faculty at GTU, Paper Setter at GTU, Life member of IETE, Sports Week (KHELDili) convener.
Current Activities	Assistant Professor Electronics and Communication Engineering Department-BIT
Specialization Areas	Signal Processing, Power Electronics, Communication Systems
Subject Taught (UG/PG)	PG : Digital Signal Processing Algorithm, Statistical Signal Analysis, Advanced Digital Signal Processing UG: Digital Signal Processing, Power Electronics, Digital Communication, Electronics & Communication, Electronics Measurement & Instrumentation, Signal & System, Control Theory, Digital Electronics, Circuit & Network, Basic Electronics
Qualifications	Ph.D. Pursuing at C.U. Shah University, Wadhwan M.E. (Industrial Electronics) (2007) (First class) M.S. University - Vadodara B.E. (Electronics) (2002) (First class) M.S. University - Vadodara B.Sc. (Maths) (1999) (First class) M.S. University - Vadodara
Skills	Well versed in simulation software like Matlab, Sci-Lab.
Achievements	Nil
Research Project	Nil
Publications	1. “Skyscraper’s Glass Cleaning Automated Robot” International Journal of Scientific and Engineering Research (ISSN: 2229-5518) Volume 4, Issue 7, July 2013. 2. “VOICE ANALYSIS BASED DISEASES IDENTIFICATION USING SPEECH PROCESSING” Journal of Emerging Technologies and Innovative Research (ISSN: 2349-5162) Volume 3, Issue 1, July 2016.

3. "Review on High Voltage DC Power Supply for Pulsed Power Supply Application" International Journal of Engineering Research & Technology (ISSN: 2278-0181) Volume 6, Issue 5, May 2017.
4. "Real Time Face Tracking and Identification for Surveillance System" International Journal of Scientific Research in Science, Engineering and Technology (ISSN: 2394-4099) Volume 4, Issue 4, April 2018.

Activities and honours

- Organized two day workshop on "Electronics Circuit Simulation using SEQUEL" as a convener on 8th & 9th May 2015 at BITS Edu Campus, Vadodara.
- Organized one day workshop on "Programmable System on Chip (PSOC)" as a co-ordinator on 6th Sept. 2014 at BITS Edu Campus, Vadodara.
- Organized ISTE approved two days workshop on "ArduyouKnowLedge" as a convener on 30th & 31st Jan. 2014 at BITS edu campus, Vadodara.
- Organized ISTE approved Short Term Training Programme on "Embedded System: Approach, Application & Design" as a co-ordinator during 7th July 2008 to 11th July 2008 at PIET, Limda, Vadodara.
- Attended one week Short Term Training Program on "DIGITAL DESIGN ON FPGA & PERIPHERAL INTERFACING" during 9th May 2016 to 14th May 2016 at Parul University, Vadodara.
- Attended ISTE sponsored two week workshop on "Computer Networking" conducted by IIT Bombay at SVIT, Vasad from 28th May to 5th July, 2014.
- Attended ISTE approved Training of Trainers Course on "Digital Signal Processing & its Application" during 13-17 May. 2013 at BVM Engineering College, V. V. Nagar.
- Attended one day workshop on "Patent Search Methodology" by GTU, Ahmedabad at BITS edu campus, Vadodara on 20th Oct. 2012.
- Attended WIPRO sponsored workshop on "High Impact Teaching Skills" under Mission 10X at Babaria Institute of Technology, Vadodara during 22nd Feb. 2010 to 26th Feb. 2012.
- Attended AICTE sponsored National Level Winter School on "TEACHING WITH MATLAB AND SIMULINK" during 21-25 Dec. 2009 at SVNIT, Surat.
- Attended one day Seminar on "Valuation of Assets: Widening Horizons" by Institution of Valuers, Vadodara at PIET, Limda on 1st March, 2008.
- Attended one day workshop on "Energy – The Urja and Emerging Opportunities in the Energy Sector" by Gujarat Energy Development Agency at PIET, Limda, Vadodara on 12th Oct. 2007.

Grant Received

- Rs 30000 received for two day Workshop on "Electronics Circuit Simulation using SEQUEL" at BITS Edu Campus, 8th - 9th May, 2015 from GUJCOST.