

Name of Faculty	Prof. Mansi Rastogi
Designation	Assistant Professor
Email id	mansirastogi.ec@bitseducampus.ac.in
Contact number	9979572831
Total Experience	
Teaching	10 years 04 months
Industry	Nil
Professional Summary	Currently Work as an Assistant Professor at Babaria Institute of Technology, Vadodara in Electronics & Communication Department since March 2015. Previously associate with NBA Accredited College affiliated from Uttar Pradesh Technical University, Lucknow. Part of various Cultural and Technical events held at college. Handle various folders like, Departmental Mentor Coordinator and Class Coordinator, Department Ganesh Utsav Coordinator, Attendance Coordinator and Student Achievement Coordinator.
Current Activities	Event Coordinator at Aahvan (GTU Zonal Event), Departmental Mentor Coordinator and Class Coordinator
Specialization Areas	Wireless Communication
Subject Taught (UG/PG)	Electromagnetic Field theory VLSI Design Digital Communication Fundamentals of Image Processing Basic of Electrical Engineering
Qualifications	ME- Electronics & Communication (First Class with Distinction) Panjab University-2014 B.TECH- Electronics & Communication (First Class with Distinction) U.P.T.U. Lucknow-2005
Skills	Competent in programming language and software like VHDL, C and Assembly language programming, Matlab, Xilinx ISE tool, Acceldsp, Microwind, Pspice, and Multisim
Achievements	Secured 2nd rank and got scholarship at college level in the university examination.
Research Project	Nil
Publications	<ol style="list-style-type: none"> 1. "Re-configurable Optimized Area CTC Codec for Wireless Applications," at Proceedings of the International Conference on Advances in Computing, Communications and Informatics (ICACCI-2014), GCET, Greater Noida, pp. 2549-2553, September 2014 (<i>Archive in IEEE Explore</i>) 2. "Re-configurable Optimized Area Turbo Decoder for Wireless Applications," at International Organization of Scientific Research Journal of Electronics and Communication Engineering (IOSR-JECE), Vol. 9, Issue 2, pp. 68-73, Mar-Apr 2014. 3. "Area Efficient Turbo Encoder for Wireless Applications on FPGA," at International Journal of Electronics Communication and Computer Engineering, Vol. 5, Issue 2, pp. 426-430, April 2014. 4. "Design and Simulation of Turbo Codec for Error Reduction Using MSOVA Algorithm," at International Journal of Electronics Communication and Computer Engineering, Vol. 4, Issue 5, pp. 1535-1540, Sep-Oct 2013.

5. "FPGA Based Area Efficient Turbo Decoder for Wireless Communication," at International Journal of Engineering Research and Applications, Vol. 3, Issue 5, pp. 1899-1904, Sep-Oct 2013.
6. "FPGA based Design of TBCC HARQ Encoder and Decoder for wireless Communication," at Proceedings of the International Conference on Communications, Networking and Signal Processing (ICCNASP-2013), VIT University, Vellore, pp. 308-311, September 2013.
7. "Hardware Implementation of Digital Watermarking," at Proceedings of the National Conference on Advances in VLSI, Embedded & Communication (AVEC-2010), AFSET College, Faridabad, pp. 1-8, August 2010.
8. "The Future of Brain Machine Interface (Mind Control Robot)," at Proceedings of the National Conference on Technological Innovations, BRCM College, Bhiwani, pp. 402-407, October 2008.

Activities and honours

- Probability and Random Variables for Wireless Communication at Proceedings of the "NPTEL online certification, sponsored by Aricent, October 2015.
- Attended GUJCOST Sponsored two day seminar on "Recent Trends on Power Electronics & Embedded Systems" on 4th – 5th January, 2019 organized by EC department of BIT
- Participated in Nation Seminar on Smart Cities-Current Scenario & Future Challenges on 24& 25 March 2017 at BITS edu campus, Vadodara
- Attended FDP on Linear Integrated Circuits - A System Approach AT TEXAS INSTRUMENTS CENTRE OF EXCELLENCE on 30 MAY to 1 JUNE 2016 at MIT, Meerut.
- Attended FDP on Embedded System Design using MSP430 AT TEXAS INSTRUMENTS CENTRE OF EXCELLENCE on 2 JUNE to 4 JUNE 2016 at MIT, Meerut.
- Attended Workshop on Electronic Circuit Simulations Using SEQUEL Sponsored by Gujarat Council of Science and Technology (GUJCOST) on May 2015 at BITS edu campus, Vadodara.
- Participated in spoken tutorial on LATEX organized by BIT-OSTC IIT Bombay held in Babaria Institute of Technology.

Grant Received

- Nil