

Name of the Faculty	Mr. Punit Nandlal Sompura
Designation	Assistant Professor
Email id	Punitsompura.ee@bitseducampus.ac.in
contact number	8140067050
Total Experience	06 years 06 months
Teaching (years, months)	06 years 06 months
Industry (years, months)	NIL
Professional Summary	Working as Assistant Professor in Electrical engineering Department of BIT from 2016. Worked as Assistant Professor in RK University, Rajkot from June 2013-2016. PG Coordinator at RK. University, Rajkot. Life member of ISTE.
Current Activities	Assistant Professor of the Electrical Engineering Department-BIT, Departmental Coordinator of E-Newsletter. Departmental Exam Coordinator
Specialization Areas	Electrical Power System
Subject Taught (UG/PG)	UG Level:- (1) Electrical and Electronics Engineering (2) Electrical Power Generation (3) Electrical Power System-I (4) Electrical Power System-II (5) Industrial Instrumentation (6) Interconnected Power System (7) Power System Operation and Control PG Level:- (1) Power System Dynamics, Control and Operation (2) Power System Analysis and Dynamics (3) Advanced Power System Protection
Qualifications	M.Tech (Electrical Power System) (2013) (First class with distinction) Institute of Technology, Nirma University, Ahmedabad B.Tech (Electrical Engineering) (2010) (First class with distinction), Arya College of Engineering and I.T, Jaipur, Rajasthan
Skills	Well versed in simulation software's like MATLAB, ETAP, HOMER, word processing software's like LaTeX.
Achievements	Received "Dhankuwar Baa International Award" for Academic excellence in HSC Exam for securing 1 st Position at District Level.
Research Project	Nil
Publications	<ol style="list-style-type: none"> 1. "Modelling Of Photovoltaic Array and Examining Effects of Irradiation in MATLAB/SIMULINK" International Journal of Research in Advent Technology, Volume 2, Issue 1, January 2014, E-ISSN: 2321-9637. 2. "Detail Simulink model of Photovoltaic cells and interfacing it with converter" International Journal of Emerging Technologies and Applications, In Engineering, Technology and Sciences (IJA-ETA-ETS) ISSN: 0974- 3588, JAN '14 Volume 7: Issue 1".

3. "Analysis of MPPT for wind energy conversion system using ANN" , International Journal Of Advanced Research In Electrical, Electronics And Instrumentation Engineering, Vol. 4, Issue 4, April 2015, ISSN (Print) : 2320 – 3765.
4. "A Review Paper for Wind and Solar Hybrid Generation for Standalone System", International Journal of Advanced Research in Engineering and Science & Technology, E-ISSN: 2393-9877, P-ISSN: 2394-2444. Special Issue for ITECE- 2016.

Activities and honours

1. Attended in the all India seminar on "Innovation and Recent Trends in Power system and Power Electronics" in 24th and 25th April, 2010.
2. Attended 2nd International conference on "Current trends and technology", organized by Nirma University on Dec 8, 2011.
3. Attended 3rd International conference on Engineering, NUiCONE-2012, organized by Nirma University on Dec 8, 2012.
4. Participated in the "Project Exhibition Cum Competition", organized by Institute of Technology, Nirma University, Ahmedabad on April 22, 2013.
5. Attended a national workshop on "Modern Trends in Power System Analysis" during March 07-08, 2013, organized by department of electrical engineering, Institute of Technology, Nirma University.
6. Completion of "Scilab workshop" organized at Institute of Technology, Nirma University by passing online exam, conducted remotely from IIT Bombay.
7. Attended one day "Energy Conservation Awareness Workshop" for engineering colleges of Gujarat, sponsored by GEDA and hosted by SDS, RK University, Rajkot.
8. Participated in a two week ISTE workshop on "Signal and System", Conducted by IIT Kharagpur, from 2nd to 12th January, 2014
9. Participated in one day state level seminar on "Green Electricity", organized by Vadodara Institute of Engineering, Kotambi, on 9th Dec, 2015.
10. Attended 2-Day Workshop on "Energy Conservation" conducted at Navrachna University in 2018.
11. Attended GUJCOST sponsored 2-day workshop at BITS Edu Campus on "Recent Trends in Power Electronics and Embedded System in January-2019.
12. Attended a workshop on "Internet of things" in March-2018.
13. Attended 4-days FDP on Design Engineering at VGEC GTU Ahmedabad in July 2017.

Grant Received

NIL